



# Call for Papers 25th International Symposium on High Performance Computer Architecture Feb 2019 Washington D.C.

## INDUSTRY SESSION CALL FOR PAPERS

The International Symposium on High-Performance Computer Architecture includes an industry session on the architecture of future systems technologies. The objective of this session is to provide a unique forum for industry participants to present their perspective on technical challenges facing future systems and discuss potential solutions. The discussion in this forum is expected to educate the wider computer architecture community on the challenges facing the industry and to encourage them to investigate solutions. The session will include a small number of papers selected based on depth and relevance to the HPCA audience.

Architects, designers and developers involved in some aspect of industrial computer systems design and development are invited to submit a paper describing new challenges, issues, and opportunities in the next-generation computer systems. The paper needs to provide insights, results, etc. that are unique to industry, which makes it clearly distinguishable from a regular paper\*. Authors should submit an abstract by July 27, 2018 11:59 PM EDT. They should submit the full version of

the paper by August 3, 2018, 11:59 PM EDT. No extensions will be granted. The full version should be a PDF file following the submission guidelines that will be made available at the submission website. Papers should be submitted for double-blind review. See the HPCA 2019 website: <http://hpca2019.seas.gwu.edu/> for additional information about HPCA 2019 and submission details. Topics of interest include, but are not limited to:

- Processor, cache and memory architectures
- Parallel/multi-core architectures
- GPUs
- Domain-specific accelerators
- Power-efficient architectures
- Dependable/secure architectures
- Analysis and exploitation of security vulnerabilities
- High-performance I/O systems
- Embedded, IoT, reconfigurable, and heterogeneous architectures
- Interconnect and network interface architectures
- Architectures for cloud, HPC, and data centers
- Innovative hardware/software trade-offs
- Impact of compilers and system software on architecture

## TENTATIVE DATES

- Performance/power modeling and evaluation
  - Architectures for emerging technology and applications
- Abstract submission : July 27, 2018

Full paper submission : August 3, 2018

Notification: November 6, 2018

Conference: February 16-20, 2019

## ORGANIZERS

General Chairs	Prof. Ahmed Louri (George Washington University) Prof. Guru Venkataramani (George Washington University)
Industry Session Chairs	Christopher Hughes (Intel Labs) Niti Madan (AMD Research)
Program Committee	Berkin Akin, Intel Sara Baghsorkhi, Intel Yuxin Bai, Apple Chris Fletcher, UIUC Michael Healy, IBM Michael Huang, Rochester Onur Kayiran, AMD Samira Khan, UVA Helen Li, Duke Heiner Litz, UC Santa Cruz Ben Serebrin, Google Sophia Shao, Nvidia Rami Sheikh, Qualcomm James Tuck, NCSU Chris Wilkerson, Nvidia

\*Industry session papers should have authors from industry but may also have academic co-authors if the contributions are clearly from the industry.  
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